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	Туре	L#	Hits	Search Text	DBs Time Stamp C ErE	=
	BRS	L#	8832	(memory or (storage adj device)) and (oscillatS3 same digitalS2) and controlS4		П
				and (load or lamp)	DERWENT; IBM TDB 09:33	
?	BRS	L3	5432	(memory or (storage adj device)) and (oscillatS3 same digitalS2) and (controlS4 same (load or lamp))	#US-PGPUB; USPAT; USOCR; EPO; JPO; 2005/07/19	
3	BRS	L4	4656	3 and ((power near2 switchS3) or drivS3 or (switchS3 near2 device))	US-PGPUB; USPAT; USOCR; EPO; JPO; 2005/07/19	
_	BRS	L5	674	4 and (set near2 point)	DERWENT; IBM TDB 09:33 US-PGPUB; USPAT; USOCR; EPO; IPO 2005/07/19	
	BKS	L)	014		DERWENT; IBM TDB 09:34	
	BRS	L6	433	4 and (set adj point)	US-PGPUB; USPAT; USOCR; EPO; JPO; 2005/07/19	_
;	BRS	L7	135		DERWENT; IBM TDB 09:15 US-PGPUB; USPAT; USOCR; EPO; JPO 2005/07/19	
					DERWENT; IBM TDB 09:35	
'	BRS	L8	125	7 and (IC or (integrated adj circuit))	US-PGPUB; USPAT; USOCR; EPO; JPO 2005/07/19 DERWENT: IBM TDB 10:41	
:	BRS	L9	4629	(memory or (storage adj device)) and (oscillatS3 same digitalS2) and controller		
	-	ļ.,,	1002	and (load or lamp)	DERWENT; IBM TDB 09:33	
	BRS	L10	4083	9 and ((power near2 switchS3) or drivS3 or (switchS3 near2 device))	US-PGPUB; USPAT; USOCR; EPO; JPO 2005/07/19 DERWENT; IBM TDB 09:33	
0	BRS	LII	1000	10 and ((set near2 point) or (referenceS near2 level))	US-PGPUB; USPAT; USOCR; EPO; JPO; 2005/07/19	_
1	BRS	L12	312	11 and (minimS2 near3 frequency)	DERWENT; IBM TDB 14:09 US-PGPUB; USPAT; USOCR; EPO; JPO; 2005/07/19	
ı .	BRS	LIZ	314	11 and (mmmn32 near 3 trequency)	DERWENT; IBM TDB 10:09	_
2	BRS	L14	9	13 and (load or lamp)	US-PGPUB; USPAT; USOCR; EPO; JPO; 2005/07/19	***
-	BRS	L13	13		DERWENT; IBM TDB 10:39 US-PGPUB; USPAT; USOCR; EPO; JPO; 2005/07/19	
				(memory or (storage adj device))	DERWENT: IBM TDB 11:46	
4	BRS	L15	331	(digitalS2 near3 oscillator) and ((set adj point) or (reference adj level)) and	US-PGPUB; USPAT; USOCR; EPO; JPO; 2005/07/19	•
<u>-</u> -	BRS	L17	200	(memory or (storage adi device)) 15 and load	DERWENT; IBM TDB 10:39 US-PGPUB; USPAT; USOCR; EPO; JPO; 2005/07/19	-
					DERWENT; IBM TDB 10:41	_
6	BRS	L19	168	17 and (IC or (integrated adj circuit))	US-PGPUB; USPAT; USOCR; EPO; JPO; 2005/07/19	
7	BRS	L18	171	16 and (IC or (integrated adj circuit))	DERWENT; IBM TDB 10:41 US-PGPUB; USPAT; USOCR; EPO; JPO; 2005/07/19	_
		ļ	ļ		DERWENT; IBM TDB 11:28	
8	BRS	L16	208	15 and (load or lamp)	US-PGPUB; USPAT; USOCR; EPO; JPO 2005/07/19 DERWENT; IBM TDB 11:40	
9	BRS	L28	3405	(ballast near2 controlS4)	US-PGPUB; USPAT; USOCR; EPO; JPO; 2005/07/19	-
_	nnc	1.20	1271		DERWENT; IBM TDB 11:41	_
0	BRS	L30	371	28 and memory	US-PGPUB; USPAT; USOCR; EPO; JPO 2005/07/19 DERWENT; IBM TDB 11:42	
1	BRS	L31	4		US-PGPUB; USPAT; USOCR; EPO; JPO; 2005/07/19	-
2	BRS	L33	1		DERWENT; IBM TDB 11:46 US-PGPUB; USPAT; USOCR; EPO; JPO; 2005/07/19	_
			<u> </u>	level))) and (memory or (storage adj device))	DERWENT; IBM TDB 11:46	
3	BRS	L32	85	30 and oscillator	US-PGPUB; USPAT; USOCR; EPO; JPO; 2005/07/19	•
4	BRS	L35	208	(digital near2 controlS3 near2 oscillator) and (memory or (storage adj device))	DERWENT; IBM TDB 12:27 US-PGPUB; USPAT; USOCR; EPO; JPO; 2005/07/19	-
				and controller	DERWENT; IBM TDB 13:56	
25	BRS	L36	105	35 and (lamp or loadS3)	US-PGPUB; USPAT; USOCR; EPO; JPO; 2005/07/19 DERWENT; IBM TDB 12:38	
26	BRS	L37	153		US-PGPUB; USPAT; USOCR; EPO; JPO; 2005/07/19	_
17	BRS	L38	ļ, —	and (lamp or load) 37 and (ballast same control\$4)	DERWENT; IBM TDB 14:28 US-PGPUB; USPAT; USOCR; EPO; JPO; 2005/07/19	_
		<u></u>	1		DERWENT; IBM TDB 13:57	-
28	BRS	L39	2	37 and ballast and controlS4	US-PGPUB; USPAT; USOCR; EPO; JPO; 2005/07/19	^
29	BRS	L40	2	37 and ballast	DERWENT; IBM TDB 14:09 US-PGPUB; USPAT; USOCR; EPO; JPO; 2005/07/19	
				· ·	DERWENT; IBM_TDB 14:09	
30	BRS	L41	2	40 and ((set near2 point) or (referenceS near2 level))	US-PGPUB; USPAT; USOCR; EPO; JPO; 2005/07/19 DERWENT; IBM TDB 14:10	
31	BRS	L42	70			-
	J	<u></u>	<u></u>		DERWENT; IBM_TDB 14-28	_
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